

**REMARKS**

Claims 1, 3, 5, 7, and 9 are presently pending and stand rejected. Claims 2, 4, 6, and 8 are cancelled without prejudice.

Claims 1 and 5 were rejected under 35 U.S.C. § 103(a) as being anticipated by Yoshioka in view of Uchida.

Claim 1 is amended to recite, among other limitations "writing a matrix of decoded luminance pixels to a first portion of the memory; writing a first matrix of decoded chrominance pixels to a second portion of the memory; writing a second matrix of decoded chrominance pixels to a third portion of the memory; and the first portion, second portion, and third portion being contiguous".

Examiner has indicated that Yoshioka is silent in regards to explicit of, 'writing a matrix of luminance pixels to a first portion of the memory; writing a first matrix of chrominance pixels to a second portion of the memory, writing a second matrix of chrominance pixels to a third portion of the memory, and the first portion second portion, and third portion being contiguous." Office Action at 3.

However, Examiner has indicated that:

Uchida '486 teaches the above-mentioned claimed limitations are well known in the art. In particular, Uchida '486 teaches, writing a matrix of luminance to a first portion of the memory (i.e., as shown in figs. 3 and 11, the DCT blocks of luminance matrix Y are stored in the respective memory areas assigned to, e.g., first portion of memory, see col. 7, lines 27 – 41 and lines 55 – 60); writing a first matrix of chrominance to second portion of the memory (i.e., as shown in figs. 3 and 11, the DCT blocks of the color difference R-Y, e.g., first matrix of chrominance (Cr) stored in the respective memory area assigned thereto, e.g., second portion of memory, see col. 7, lines 26 – 41 and lines 55 – 60); writing a matrix of second chrominance to a third portion of the memory (i.e., as shown in fig. 11, the DCT blocks of second chrominance matrix B-Y (Cb) written in the respective memory area assigned thereto, e.g., third portion of the memory, see col. 7, lines 27 – 41 and lines 55 – 60); and the first portion, second portion, and third portion being contiguous (i.e., as shown in fig. 11, the first portion (Y), second portion (R-Y) and third portion (B-Y) of the memory are arranged in continuous/contiguous form).

Office Action at 3.

Assignee respectfully submits, however, that Uchida does not teach “writing a matrix of decoded luminance pixels to a first portion of the memory; writing a first matrix of decoded chrominance pixels to a second portion of the memory; writing a second matrix of decoded chrominance pixels to a third portion of the memory; and the first portion, second portion, and third portion being contiguous”.

Assignee calls Examiner's attention to Col. 7, Lines 43-60:

Referring to Fig. 11, the coded data of one DCT block of the luminance signal Y is stored in the memory area of the memory 99 assigned to the luminance signal data 112. In the storing operation, the data of a 'low frequency component' of the luminance signal data 112 is given priority. ... Moreover, the coded data of the respective DCT blocks of the color difference signal (R-Y) and the color difference signal (B-Y) are stored in the respective memory area assigned thereto in the memory 99.

Accordingly, Assignee respectfully requests that Examiner withdraw the rejection to claims 1, 3, 5, and 7, as well as to dependent claim 9.

Additionally, claim 9 is added. Notwithstanding claims 1, 3, 5, and 7, Assignee respectfully submits that claim 9 should be allowed. Although Examiner has indicated that Brooks "teaches plurality of memory sections being independently controlled to store blocks of data words thus is consider as second portion and the third portion form portions of a plurality of data words", Brooks does not teach "wherein one portion of a single data word is part of the second portion and another portion of the single data word is part of the third portion form" Accordingly, Assignee respectfully requests allowance for claim 9.

**CONCLUSION**

For at least the foregoing reasons, Assignee respectfully submits that each of the pending claims are allowable and Examiner is respectfully requested to pass this case to issuance. The Commissioner is hereby authorized to charge additional fees or credit overpayments to the deposit account of McAndrews, Held & Malloy, Account No. 13-0017.

Dated: November 13, 2008

Respectfully submitted,

A handwritten signature in dark ink, appearing to read 'Mirut Dalal', is written over a horizontal line.

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